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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/990,474	11/21/2001	Yuji Judai	MTS-2700US1	3161
7590 01/30/2004			EXAMINER	
Ratner & Prestia			WEISS, HOWARD	
P.O. Box 980			ADTURNE	DARED MUARED
Valley Forge, P	A 19482	4	ART UNIT	PAPER NUMBER
			2814	

DATE MAILED: 01/30/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
Office Action Summary	09/990,474	JUDAI, YUJI				
,	Examiner Howard Weiss	Art Unit				
Th MAILING DATE of this communication app		- · · · · · · · · · · · · · · · · · · ·				
Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a repl - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statute - Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b). Status	36(a). In no event, however, may a reply be timely within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. O (35 U.S.C. § 133).				
_	1)⊠ Responsive to communication(s) filed on <u>11 December 2003</u> .					
2a) ☐ This action is FINAL . 2b) ☒ This	action is non-final.					
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims						
 4) ☐ Claim(s) 5,11,12,14 and 17-21 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 5,11,12,14 and 17-21 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or election requirement. 						
Application Papers						
9)☐ The specification is objected to by the Examine 10)☐ The drawing(s) filed on is/are: a)☐ acc Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct	cepted or b) objected to by the liderawing(s) be held in abeyance. See tion is required if the drawing(s) is objected to by the liderawing(s) is objected to by the liderawing(s).	e 37 CFR 1.85(a). ected to. See 37 CFR 1.121(d).				
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. §§ 119 and 120 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) ☐ All b) ☐ Some * c) ☐ None of: 1. ☐ Certified copies of the priority documents have been received. 2. ☐ Certified copies of the priority documents have been received in Application No 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 13) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application) since a specific reference was included in the first sentence of the specification or in an Application Data Sheet. 37 CFR 1.78. a) ☐ The translation of the foreign language provisional application has been received. 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121 since a specific reference was included in the first sentence of the specification or in an Application Data Sheet. 37 CFR 1.78.						
Attachment(s)	_					
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) 1	5) Notice of Informal P	(PTO-413) Paper No(s) atent Application (PTO-152)				

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Attorney's Docket Number: MTS-2700US1

Filing Date: 10/22/98

Continuing Data: Division of 09/177,038 (10/22/98, now abandoned);

RCE established 12/11/03

Claimed Foreign Priority Date: 10/24/97 (JPX)

Applicant(s): Judai

Examiner: Howard Weiss

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Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 12/11/03 has been entered.

Claim Rejections - 35 USC § 103

- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. Claims 11, 12 and 16 to 21 are rejected under 35 U.S.C. 103(a) as being unpatentable over Arita et al. (U.S. Patent No. 5,624,864) and Kishida et al. (JP 08-022990).

Arita et al. show most aspects of the instant invention (e.g. Figure 20) including:

> providing a circuit board **31** and forming a first insulating film **37** at least indirectly on said circuit board

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➤ forming a ferroelectric capacitor 41 by forming a lower electrode 38, a ferroelectric film 39 and an upper electrode 40 on said first insulating film

- forming a second insulating film 46 and forming a plurality of contact openings 43b, 43c to said upper and lower electrodes
- ➢ forming a metal wiring pattern in said openings including a base layer 54b, 54c of TiN and an upper layer 44b, 44c of Al
- > forming a surface protective film **55** of SiN over said second insulating film and metal wiring pattern

Arita et al. do not show heating-treating the TiN layer before depositing the upper Al layer and deposing directly the Al layer on said TiN layer using sputtering and heating the circuit board in a temperature rang of 100 to 400° C. Kishida et al. teach (e.g. Figures 1) to heating-treat (i.e. anneal) the TiN layer 5 before directly depositing the Al upper layer 6 using sputtering and heating the circuit board in a temperature rang of 100 to 400° C to improve step-difference coverage of an aluminum alloy film in a contact hole. (see Purpose). It would have been obvious to a person of ordinary skill in the art at the time of invention to heating-treat (i.e. anneal) the TiN layer before directly depositing the Al upper layer using sputtering and heating the circuit board in a temperature rang of 100 to 400° C as taught by Kishida et al. in the process of Arita et al. to improve step-difference coverage of an aluminum alloy film in a contact hole.

4. Claims 5, 11, 12 and 16 to 21 are rejected under 35 U.S.C. 103(a) as being unpatentable over Patel et al. (U.S. Patent No. 5,374,578) and Kishida et al.

Patel et al. show most aspects of the instant invention (e.g. Figures 1 to 11) including:

providing a circuit board 1 and forming a first insulating film 10 at least indirectly on said circuit board Art Unit: 2814

- ➢ forming a ferroelectric capacitor by forming a lower electrode 12, a ferroelectric film 14 and an upper electrode 16 on said first insulating film
- ▶ forming a second insulating film 18 using TEOS-CVD method utilizing TEOS activated by O₃ (Column 5 Lines 19 to 32)
- > forming a plurality of contact openings 20, 22 to said upper and lower electrodes
- forming a metal wiring pattern 26 in said openings including a base layer of TiN and an upper layer of AI (Column 5 Lines 47 to 58)
- ▶ heat treating the TiN layer in the temperature range between 200 to 650° C (Column 5 Lines 64 to 68).
- forming a surface protective film 28 over said second insulating film and metal wiring pattern

Patel et al. do not show heating-treating the TiN layer before directly depositing the upper Al layer using sputtering and heating the circuit board in a temperature rang of 100 to 400° C. Kishida et al. teach (e.g. Figures 1) to heating-treat (i.e. anneal) the TiN layer 5 before directly depositing the Al upper layer 6 using sputtering and heating the circuit board in a temperature rang of 100 to 400° C to improve step-difference coverage of an aluminum alloy film in a contact hole. (see Purpose) It would have been obvious to a person of ordinary skill in the art at the time of invention to heating-treat (i.e. anneal) the TiN layer before directly depositing the Al upper layer using sputtering and heating the circuit board in a temperature rang of 100 to 400° C as taught by Kishida et al. in the process of Patel et al. to improve step-difference coverage of an aluminum alloy film in a contact hole.

5. Claim 14 is rejected under 35 U.S.C. 103(a) as being unpatentable over Arita et al. and Kishida et al., as applied to Claim 16 above, and in further view of Wolf et al. (1986).

Arita et al. and Kishida et al. show most aspects of the instant invention (Paragraph 3) except for depositing the SiN layer using PECVD at RF power 300 W or less. Wolf

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et al. teach to deposit the SiN layer using PECVD so as to use low temperatures (Middle paragraph of page 192). It would have been obvious to a person of ordinary skill in the art at the time of invention to deposit the SiN layer using PECVD as taught by Wolf et al. in the process of Arita et al. and Kishida et al. so as to use low temperatures.

It would have been obvious to one having ordinary skill in the art at the time the invention was made to use RF power of 300 W or less, since it has been held that where the general conditions of a claim are disclosed in the prior art, discovering the optimum or workable ranges involves only routine skill in the art. *In re Aller*, 105 *USPQ* 233.

6. Claim 14 is rejected under 35 U.S.C. 103(a) as being unpatentable over Patel et al. and Kishida et al., as applied to Claim 16 above, and in further view of Wolf et al.

Patel et al. and Kishida et al. show most aspects of the instant invention (Paragraph 4) except for depositing the SiN layer using PECVD at RF power 300 W or less. Wolf et al. teach to deposit the SiN layer using PECVD so as to use low temperatures (Middle paragraph of page 192). It would have been obvious to a person of ordinary skill in the art at the time of invention to deposit the SiN layer using PECVD as taught by Wolf et al. in the process of Patel et al. and Kishida et al. so as to use low temperatures.

It would have been obvious to one having ordinary skill in the art at the time the invention was made to use RF power of 300 W or less, since it has been held that where the general conditions of a claim are disclosed in the prior art, discovering the optimum or workable ranges involves only routine skill in the art. *In re Aller*, 105 USPQ 233.

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Response to Arguments

7. Applicant's arguments with respect to Claims 5, 11, 12, 14 and 16 to 21 have been considered but are most in view of the new ground(s) of rejection.

Conclusion

- 8. Papers related to this application may be submitted directly to Art Unit 2814 by facsimile transmission. Papers should be faxed to Art Unit 2814 via the Art Unit 2814 Fax Center located in Crystal Plaza 4, room 3C23. The faxing of such papers must conform with the notice published in the Official Gazette, 1096 OG 30 (15 November 1989). The Art Unit 2814 Fax Center number is (703) 872-9306. The Art Unit 2814 Fax Center is to be used only for papers related to Art Unit 2814 applications. The official TC2800 Before-Final, (703) 872-9318, and After-Final, (703) 872-9319, Fax numbers will provide the fax sender with an auto-reply fax verifying receipt of their fax by the USPTO.
- 9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Howard Weiss at (571) 272-1720 and between the hours of 8:00 AM to 4:00 PM (Eastern Standard Time) Monday through Friday or by e-mail via Howard.Weiss@uspto.gov.

Any inquiry of a general nature or relating to the status of this application should be directed to the Group 2800 Receptionist at **(703) 308-0956**.

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10. The following list is the Examiner's field of search for the present Office Action:

Field of Search	Date
U.S. Class / Subclass(es): 438/ 3, 240	thru 1/21/04
Other Documentation: none	
Electronic Database(s): EAST	thru 1/21/04

HW/hw 21 January 2004 Howard Weiss Examiner Art Unit 2814